

Google patents

determine used or new memory chip

Search Patents

[Advanced Patent Search](#)Try uppercase "OR" to search for either of two terms. [\[details\]](#)

Patents

Patents 1 - 10 on **determine used or new memory chip**. (0.11 seconds)

## › List view

[Cover view](#)

## › Sort by relevance

[Sort by date \(new first\)](#)[Sort by date \(old first\)](#)

## › Any status

[Issued patents](#)[Applications](#)[System and method for reprogramming vehicle computers](#)

US Pat. 5278759 - Filed May 7, 1991 - Chrysler Corporation

It should also be before the flash **memory chip** is erased. .... This ID number is **used to determine** which stored. erase and reprogramming algorithms should ...[Method and apparatus for substituting spare \*\*memory chip\*\* for malfunctioning ...](#)

US Pat. 5267242 - Filed Sep 5, 1991 - International Business Machines Corporation

In step 604, the HAMT determines if the **new chip** row indicator is on ... card being scrubbed to **determine** whether the spare **chip** has already been **used**. ...[Fast on-\*\*chip\*\* current measurement circuit and method for use with \*\*memory\*\* ...](#)

US Pat. 6031777 - Filed Jun 10, 1998 - Integrated Silicon Solution, Inc., Nexflash Technologies, Inc.

2, the on-**chip** current measurement circuit is typically **used** by an external circuit tester 200 to measure **memory** cell current and to **determine memory** cell ...[Synergistic multiple bit error correction for \*\*memory\*\* of array \*\*chips\*\*](#)

US Pat. 5418796 - Filed Mar 26, 1991 - International Business Machines Corporation

395-401, disclosed a **new** class of SEC-DED codes which provided an improvement of ... Nevertheless, Miller suggests that his **memory chip** could be **used** with ...[Memory card design with parity and ECC for non-parity and non-ECC systems](#)

US Pat. 6185718 - Filed Feb 27, 1998 - International Business Machines Corporation

RAM is generally **used** by a processor for reading and writing data. ROM is generally **used** for storing data which ... Furthermore, the typical **memory chip** ...[Plural \*\*memory\*\* controller apparatus](#)

US Pat. 4016545 - Filed Jul 31, 1975 - Harris Corporation

In an MKH **chip**, F is **used** to inhibit the the same **chip** as a **memory** ... As stated above, the page timer TIME is **memory** later. **used** for **determining** which page ...[Real time cache implemented by on-\*\*chip\*\* \*\*memory\*\* having standard and cache ...](#)

US Pat. 5586293 - Filed Jan 17, 1995 - Motorola, Inc.

It can be **used** to prevent consistency problems when an external device loads **new** code into the external program **memory**. A cache bypass mechanism is also ...[Method for adaptive decoding of \*\*memory\*\* addresses](#)

US Pat. 6260121 - Filed Jun 30, 1998 - Micron Technology, Inc.

This has recently led to the development of **new memory chip** designs, ... This information is **used to determine** if a subsequent **memory** access is directed to ...[Apparatus for adaptive decoding of \*\*memory\*\* addresses](#)

US Pat. 6253276 - Filed Jun 30, 1998 - Micron Technology, Inc.

This has recently led to the development of **new memory chip** designs, ... and to store information that can be **used to determine** upper and lower address ...